hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

By: \_\_\_\_\_

Date: December \$2004

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No.

10/646,218

Confirmation No: 1417

Applicant Filed

Manfred Engelhardt August 22, 2003

Title

Method of Producing an Integrated Circuit Configuration

Art Unit

2814

Examiner Docket No.

Dilinh P. Nguyen GR98P2661 D

Customer No. :

24131

## INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner for Patents

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

Patent Abstracts of Japan JP 9 181 180 (Liou et al.), dated June 25, 1997, and corresponding United States Patent No. 5,847,460, dated December 8, 1998;

Patent Abstracts of Japan JP 11 317 446 A (Mori et al.), dated November 16, 1999, and corresponding United States Patent No. 6,335,570 B2, dated January 1, 2002.

As per the Notice in 1273 OG 55 (August 5, 2003) no copies of any abovementioned U.S. patents and U.S. patent application publications are submitted for any application filed after June 30, 2003.

Respectfully submitted,

For Applicant

WERNER H. STEMER REG. NO. 34,956

Date: December 2, 2004

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U.S. DEPARTM	ENT OF	COMMERCE	GR98P2661 D 10/646,218					
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